

Amendments to the Specification:

Please replace the paragraph beginning at page 7, line 13, with the following rewritten paragraph:

In order to enhance the performance of variable threshold slicers in situations where delay spread due to multipath can cause loss of signal at one deviation of a FSK signal, it has been found beneficial to normalise the amplitude of the signal at the input to the variable threshold slicer in order to achieve optimum bit error rate (BER) when this kind of delay spread occurs. The normalisation may be effected using a circuit 78 which approximates to a 1-tap equaliser. In operation, the gain normalisation can be achieved using the maximum peak-to-peak amplitude value of the signal occurring say during the sync. code word as a reference.